

## Electronic Acknowledgement Receipt

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<b>Application Number:</b>	10091934
<b>International Application Number:</b>	
<b>Confirmation Number:</b>	7058
<b>Title of Invention:</b>	Interconnect-aware methodology for integrated circuit design
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<b>Filer:</b>	Suzanne Erez/Hadas Bar-Gil
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<b>Attorney Docket Number:</b>	IL920020007US1
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<b>Application Type:</b>	Utility under 35 USC 111(a)

### Payment information:

Submitted with Payment	no
<b>File Listing:</b>	

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1		IL920020007US1_OA30NO V07_RES04FEB08.pdf	64535 <small>641b7ebb78a5d4be6c84300842bf42d5 3c28a12</small>	yes	9
<b>Multipart Description/PDF files in .zip description</b>					
	<b>Document Description</b>		<b>Start</b>		<b>End</b>
	Amendment After Final		1		1
	Claims		2		4
	Applicant Arguments/Remarks Made in an Amendment		5		9
<b>Warnings:</b>					
<b>Information:</b>					
2	Request for Continued Examination (RCE)	IL920020007US1_RCE_04F EB08.pdf	58271 <small>5864c5dc62334a2eeb0c145e1a53df00c 50a65a71</small>	no	3
<b>Warnings:</b>					
This is not a USPTO supplied RCE SB30 form.					
<b>Information:</b>					
3	Change of Address	IL920020007US1_Chg_Corr _Add_FEB08.pdf	106014 <small>11796e990f01831058409d01e2817418b d905140</small>	no	1
<b>Warnings:</b>					
<b>Information:</b>					
<b>Total Files Size (in bytes):</b>			228820		
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